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Dr. D.K.Gautam was born in Mathura (U.P.) in 1959. He completed his doctorate degree in VLSI technology from Central Electronics

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Technology 1) Explain basic differences between Bipolar and MOS Integrated Circuits. [UTU 2010] 2) What are the advantages of Integrated circuits over discrete component circuits? [UTU 2010] 3) Give the steps used in the fabrication of ICs in a block diagram representation. [UTU 2010] Crystal Growth 4) What are the various types of the defects in

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- Low resistivity
- Easy to form
- Easy to etch for pattern generation
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- Smooth surface
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Technology 6/1-3 2  
Classification Of ICs,  
Scale of Integration  
6/4-6 3 Semiconductor,  
Monolithic and Hybrid  
IC 7/12-14 4 Features  
of ICs 6/6-7 5 Crystal  
Growth Cz Technique  
1/ 9-13, 17-27 6 Wafer  
Preparation and  
Measurements 1/31-44  
7 IC Fabrication Steps



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memory cell for  
leakage power  
dissipation”,  
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